

JC14 PCC/DC/PCT/PTO 19 MAY 2005

PTO/SB/08B (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet

1

of

1

Application Number

10/535591

Filing Date

Concurrently

First Named Inventor

VAN DE WAERDT

Art Unit

N/A

Examiner Name

N/A

Attorney Docket Number

US020465

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
Xe		"STRIDE DIRECTED PREFETCHING IN SCALAR PROCESSORS", by John FU et al., pages 102-110. Proceedings of the 25th Annual Int'l Symposium on Microarchitecture, Dec. 1992	
Xe		"HARDWARE AND SOFTWARE CACHE PREFETCHING TECHNIQUES FOR MPEG BENCHMARKS", by D. ZUCKER pp 782-796, IEEE Transactions on Circuits & Systems for Video ,Tech. V.10 Aug. 2002	
Xe		"EFFECTIVE HARDWARE-BASED DATA PREFETCHING FOR HIGH PERFORMANCE PROCESSORS", by T. CHEN et al., IEEE Transactions on Computer, Vol. 44, pp 609-623, May 1995.	

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Sheet 1 of 1

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U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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